

UTILITY PATENT APPLICATION TRANSMITTAL <i>(Only for new nonprovisional applications under 37 CFR 1.53(b))</i>	Attorney Docket No. 242278US2S DIV First Inventor or Application Identifier Ryo FUKUDA Title SEMICONDUCTOR INTEGRATED CIRCUIT HAVING LATCHING MEANS CAPABLE OF SCANNING
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APPLICATION ELEMENTS See MPEP chapter 600 concerning utility patent application contents		ADDRESS TO:	Commissioner for Patents Mail Stop Patent Application Alexandria, Virginia 22313
1. <input checked="" type="checkbox"/> Fee Transmittal Form (e.g. PTO/SB/17) (Submit an original and a duplicate for fee processing)			
2. <input checked="" type="checkbox"/> Specification	Total Sheets	<input type="text" value="53"/>	
3. <input checked="" type="checkbox"/> Drawing(s) (35 U.S.C. 113)	Total Sheets	<input type="text" value="6"/>	
4. <input checked="" type="checkbox"/> Oath or Declaration	Total Pages	<input type="text" value="2"/>	
a. <input type="checkbox"/> Newly executed (original or copy)			
b. <input checked="" type="checkbox"/> Copy from a prior application (37 C.F.R. §1.63(d)) (for continuation/divisional with box 17 completed)			
i. <input type="checkbox"/> DELETION OF INVENTOR(S)	Signed statement attached deleting inventor(s) named in the prior application, see 37 C.F.R. §1.63(d)(2) and 1.33(b).		
5. <input type="checkbox"/> CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix)			
6. <input type="checkbox"/> Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)			
a. <input type="checkbox"/> Computer Readable Form (CRF)			
b. Specification or Sequence Listing on :			
i. <input type="checkbox"/> CD-ROM or CD-R (2 copies); or			
ii. <input type="checkbox"/> Paper			
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ACCOMPANYING APPLICATION PARTS			
7. <input checked="" type="checkbox"/> Assignment recorded at Reel 012332/Frame 0807			
8. <input checked="" type="checkbox"/> Application Data Sheet. See 37 CFR 1.76			
9. <input type="checkbox"/> 37 C.F.R. §3.73(b) Statement (when there is an assignee)	<input type="checkbox"/> Power of Attorney		
10. <input type="checkbox"/> English Translation Document (if applicable)			
11. <input checked="" type="checkbox"/> Information Disclosure Statement (IDS)/PTO-1449	<input type="checkbox"/> Copies of IDS		
12. <input checked="" type="checkbox"/> Preliminary Amendment			
13. <input checked="" type="checkbox"/> White Advance Serial No. Postcard			
14. <input type="checkbox"/> Certified Copy of Priority Document(s) (if foreign priority is claimed)			
15. <input type="checkbox"/> Applicant claims small entity status. See 37 CFR 1.27			
16. <input checked="" type="checkbox"/> Other: Request for Priority			

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10/662309
09/16/03



17. If a **CONTINUING APPLICATION**, check appropriate box, and supply the requisite information below:

Continuation Divisional Continuation-in-part (CIP) of prior application no.: 09/993,603

Prior application information: Examiner: LE, T.

Group Art Unit: 2824

For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 4b, is considered a part of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

18. CORRESPONDENCE ADDRESS

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09/16/03

Docket No: 242278US2S DIV

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

INVENTOR(S) Ryo FUKUDA

SERIAL NO: New Application

FILING DATE: Herewith

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING LATCHING MEANS CAPABLE OF SCANNING

FEE TRANSMITTAL

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ALEXANDRIA, VIRGINIA 22313

FOR	NUMBER FILED	NUMBER EXTRA	RATE	CALCULATIONS
TOTAL CLAIMS	1 - 20 =	0	x \$18 =	\$0.00
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Respectfully Submitted,

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